



A world class team looking for a world class colleague!

Are you our new member of our world-class RFIC Design Team?

Sivers Wireless, a subsidiary to Sivers Semiconductors, develops, manufactures and sells chips, components, modules and subsystems based on advanced technology for 5G millimeter-wave networks. Our cutting-edge technology products enable our customers to offer faster and more reliable connections. We have completed our first generation of mmWave RFICs supporting frequency bands from 24 GHz to 71 GHz and we are now starting the development of our next generation products.

The opportunity:

Sivers Wireless is looking for two more engineers to our RFIC Design Engineering team. You are innovative and have a passion for developing RFIC transceivers.

Tasks and responsibilities:

You will:

- Perform Subsystem design of LNAs, PAs, mixers, PLLs, LO generation, baseband filters, ADCs, DACs, or other analog blocks.
- Bring your design from schematic, to layout and verification, using our capable EDA tools from Cadence, Keysight, ANSYS, and Mentor Graphics.
- Do sub-micron CMOS design and/or BiCMOS design.
- Define and participate in bring-up and validations of our products.
- Support production test.
- Support integration of RFICs into our antenna modules.

You will work with a group of 10+ experienced RFIC Design Engineers located in Gothenburg and Kista. We have well equipped labs in the same offices.

Required qualifications and experiences:

We are looking for innovative engineers with a deep burning interest in RFIC design to be applied in our world-class mmWave transceivers. Your experience shall be relevant, either from the industry or from academia. We will also consider you with less experience if you want to participate in our journey.

We value experience in any of the following areas:

- Wireless systems fundamentals, e.g., Wi-Fi, WiGig, 3GPP.
- Architecture and design of RF transceivers.
- Subsystem design of LNAs, PAs, mixers, PLLs, LO generation, baseband filters, ADCs, or DACs.
- Analog design fundamentals e.g., OPAMPs, LDOs, oscillators.
- Experience of deep sub-micron CMOS design and/or BiCMOS design.
- Knowledge about mixed-signal design and verification.
- Bring-up and validation of RFIC transceivers. Familiarity with calibration methods.
- Experience of driving chips into volume production.
- Solid understanding of CMOS processes and knowledge of device physics.
- Experience of EDA tools from Cadence, Keysight, ANSYS, and Mentor Graphics.
- MSc in Electrical Engineering or other relevant area.

We offer:

An exciting and challenging position where you get the chance to work with cutting-edge technology and state-of-the-art mmWave instruments. Together with skilled colleagues you will develop world-class RFICs for wireless communication. We are expanding our design and validation capability to meet the demand of our products in the mmWave area.

Location:

Kista or Gothenburg

Application:

Send to career@sivers-semiconductors.com. We will continuously review applications.

Sivers Semiconductors AB is a leading and internationally recognized technology company that, through its two business areas Wireless and Photonics, supplies chips and integrated modules. Wireless develops RF chips and antennas for advanced 5G systems for data and telecommunications networks. Photonics develops and manufactures semiconductor-based optical products for optical fiber networks, sensors and optical fiber communications (Li-Fi). The company is listed on Nasdaq First North Growth Market Stockholm under SIVE. The head office is located in Kista, Sweden. For more information: www.sivers-semiconductors.com