



# A world class team looking for a world class colleague!

## Are you our new Manager of RFIC Design Engineering?

Sivers Wireless, a subsidiary to Sivers Semiconductors, develops, manufactures and sells chips, components, modules and subsystems based on advanced technology for 5G millimeter-wave networks. Our cutting-edge technology products enable our customers to offer faster and more reliable connections. We have completed our first generation of mmWave RFICs supporting frequency bands from 24 GHz to 71 GHz and we are now starting the development of our next generation products.

### **The opportunity:**

Sivers Wireless is looking for a Manager of RFIC Design Engineering who is innovative and has a passion for developing RFIC transceivers.

### **Tasks and responsibilities:**

You will:

- Lead and expand the RFIC Design Engineering team of 10+ employees supported by external consultants to develop and deliver wireless transceivers for existing and future products.
- Drive innovative RF / Analog architectures, specifications and designs.
- Maintain and improve the RFIC design and verification methodologies targeting first-time silicon success.
- Support validation of test and production RFIC transceivers and drive revisions to achieve full performance.
- Support production test and reliability qualification.
- Support integration of RFICs into our antenna modules.

**Required qualifications and experiences:**

At least 10 years of industry experience in RFIC design engineering.

At least 5 years of engineering management experience, including leadership of RFIC design teams.

You have extensive technical background in several of the following areas:

- Wireless systems fundamentals, e.g. Wi-Fi, WiGig, 3GPP.
- Architecture and design of RF transceivers.
- Subsystem design of LNAs, PAs, mixers, PLLs, LO generation, baseband filters, ADCs, or DACs.
- Analog design fundamentals e.g., OPAMPs, LDOs, oscillators.
- Experience of deep sub-micron CMOS design and/or BiCMOS design.
- Knowledge about mixed-signal design and verification.
- Bring-up and validation of RFIC transceivers. Familiarity with calibration methods.
- Experience of driving chips into volume production.
- Solid understanding of CMOS processes and knowledge of device physics.
- Experience of EDA tools from Cadence, Keysight, ANSYS, and Mentor Graphics.

**Location:**

Kista or Gothenburg

**Application:**

Send to [career@sivers-semiconductors.com](mailto:career@sivers-semiconductors.com). We will continuously review applications.

**Sivers Semiconductors AB** is a leading and internationally recognized technology company that, through its two business areas Wireless and Photonics, supplies chips and integrated modules. Wireless develops RF chips and antennas for advanced 5G systems for data and telecommunications networks. Photonics develops and manufactures semiconductor-based optical products for optical fiber networks, sensors and optical fiber communications (Li-Fi). The company is listed on Nasdaq First North Growth Market Stockholm under SIVE. The head office is located in Kista, Sweden. For more information: [www.sivers-semiconductors.com](http://www.sivers-semiconductors.com)